

ABSTRACT OF THE DISCLOSURE

A drive power supply circuit for driving liquid crystal display of the present invention generates necessary levels in an LCD drive power supply circuit that generates drive levels for LCDs in an LCD controller/driver IC by means of switching connection to capacitors in a constant manner or in synchronism with the timing of LCD driving. It allows reduction in number of the components such as amplifiers for level generation and external capacitors, which in turn reduces current consumption of the entire system, chip areas, and mounting areas.

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